Design & Simulation of R Slotted Patch Antenna with EBG Substrates

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Abstract:-
This paper presents the design and simulation of rectangular & circular with EBG substrate using High Frequency Structure Simulator (HFSS). The two different configurations of broadband microstrip patch antenna, multistacked rectangular patch antenna & multistacked circular patch antenna are analyzed. Return Loss & Gain are performance parameter. The substrate used in these two configurations is RT Duroid having dielectric constant 2.2. The result calculated is return loss of rectangular patch & circular patch with EBG substrates are -13.98 dB & -43.4437 dB and gain of rectangular patch & circular patch with EBG substrates are 5.7478 dB & 10.4954 dB respectively.

Keywords –Rectangular Patch, Circular Patch, High Frequency Structured Simulator (HFSS).

Introduction
Microstrip antenna is an ideal choice for wireless communication due to low profile, light weight, low cost, simplicity of manufacturing and easy integration to circuit. However, conventional microstrip patch antenna suffers from very narrow bandwidth [1], typically about 5% bandwidth with respect to central frequency.

In high performance aircraft, spacecraft, satellite, and missile applications, where size, weight, cost, performance, ease of installation, and aerodynamic profile are constraints, low-profile antennas may be required. Presently there are many other government and commercial applications, such as mobile radio and wireless communications that have similar specifications. To meet these requirements, microstrip antennas [2] can be used. These antennas are low profile, conformable to planar and nonplanar surfaces, simple and inexpensive to manufacture using modern printed-circuit technology, mechanically robust when mounted on rigid surfaces, compatible with MMIC designs, and when the particular patch shape and mode are selected, they are very versatile in terms of resonant frequency, polarization, pattern, and impedance. In addition, by adding loads between the patch and the ground plane, such as pins and varactor diodes, adaptive elements with variable resonant frequency, impedance, polarization, and pattern can be designed.

II. Antenna Design
In this section, structure of patch antennas are discussed, EBG rectangular patch, EBG Circular patch antenna are simulated by HFSS software in order to decrease return loss and increase gain.

A. Rectangular Patch Antenna
A Rectangular patch antenna is designed and simulated here. Dimensions given in fig. 1. of patch are calculated using [3] and optimized dimensions are L=21.388mm, W=20.388mm. Substrate with dielectric constant 2.2 and height 1.588 mm, x=3.126 mm. Patch is designed for operating frequency 10.5 GHz. Dimension of ground plate is calculated using following equation

\[ W_g = 6h + w \]
\[L_g = 6h + L\]

For designing the rectangular patch following calculations is done with help of given equations [9]

\[W = \frac{V_0}{2f_r} \sqrt{\frac{2}{\varepsilon_{\text{eff}}}}\]

\[V_0 = \text{free space velocity of light}\]

\[\varepsilon_{\text{eff}} = \frac{\varepsilon_{\text{r}+1}}{2} + \frac{\varepsilon_{\text{r}-1}}{2} \left[1 + 12 \frac{h}{w}\right]^{1/2}\]

\[\frac{\Delta L}{h} = 0.412 \left(\frac{\varepsilon_{\text{eff}} + 0.3}{\varepsilon_{\text{eff}} - 0.258}\right)\left(\frac{w}{h} + 0.8\right)\]

\[L = \frac{\lambda}{2} - 2\Delta L\]

\[L_{\text{eff}} = L + 2\Delta L\]
B. Circular Patch Antenna

A circular patch antenna is designed and simulated here. Dimensions given in fig. 5 of patch are calculated using [3] and optimized dimensions are $a = 30\text{mm}$. Substrate with dielectric constant 2.2 and height 1.588 mm, $x=4.8\text{ mm}$. Patch is designed for operating frequency 10.5 GHz. Dimension of patch is calculated using given equations

$$a = \frac{F}{\left\{1 + \frac{2h}{\pi \varepsilon_r F} \left[\ln \left(\frac{\pi F}{2h}\right) + 1.7726\right]\right\}^{1/2}}$$

$$F = \frac{8.791 \times 10^9}{f_r \sqrt{\varepsilon_r}}$$

After Simulation return loss is obtained -43.4437 db with gain 10.4954 db. Frequency Vs Return Loss plot & radiation pattern is shown in fig. 6 & fig. 7 respectively.
Electric field & magnetic field also play very important role to design patch antenna. The distribution of magnetic field is given in fig 8. Maximum magnetic field is 1.1534 A/m

III. Comparative Analysis
In this section, comparative of two configurations is shown in tabular form. Return loss and bandwidth is compared in table 1.

### Table 1. Comparative Analysis of Different Configurations of Antenna

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Parameters</th>
<th>Rectangular Patch</th>
<th>Circular Patch</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Return Loss</td>
<td>-13.98</td>
<td>-43.4437</td>
</tr>
<tr>
<td>2.</td>
<td>Resonant Frequency ($f_0$)</td>
<td>10.5</td>
<td>10.5</td>
</tr>
<tr>
<td>3.</td>
<td>Lower cutoff Frequency ($f_L$)</td>
<td>10.2325</td>
<td>10.3374</td>
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<tr>
<td>4.</td>
<td>Upper Cutoff Frequency ($f_H$)</td>
<td>10.7781</td>
<td>10.6473</td>
</tr>
<tr>
<td>5.</td>
<td>Bandwidth</td>
<td>0.5456</td>
<td>0.3099</td>
</tr>
<tr>
<td>6.</td>
<td>Gain</td>
<td>5.7478</td>
<td>10.4954</td>
</tr>
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</table>

IV. Conclusion
After Simulation, it is found that Circular patch with c slot antenna has low return loss with high gain and bandwidth. Simulated return loss is -43.4437 db with gain 10.495 db.

References


